



Gokaraju Rangaraju Institute of Engineering and Technology  
(Autonomous)

**GR18  
D I- II S**

**M.Tech. I Year II Semester Supplementary Examinations (GR18) Time Table August/September 2025**

*Timings: 10.00 AM TO 01.00 PM*

<b>Date Branch</b>	<b>25/08/2025, FN (Monday)</b>	<b>28/08/2025, FN (Thursday)</b>	<b>30/08/2025, FN (Saturday)</b>	<b>02/09/2025, FN (Tuesday)</b>	<b>04/09/2025, FN (Thursday)</b>
<b>STRU (20)</b>	<b>GR18D5174</b> FEM Structural engineering	<b>GR18D5175</b> Structural Dynamics	<b>GR18D5177</b> Design of Formwork	<b>GR18D5180</b> Design of Advanced Concrete Structures	<b>GR18D5208</b> Disaster Management
<b>PE (43)</b>	<b>GR18D5073</b> Modeling and Analysis of Electrical Machines	<b>GR18D5074</b> Digital Control of Power Electronic and Drive Systems	<b>GR18D5058</b> FACTS and Custom Power Devices	<b>GR18D5063</b> Smart Grids	<b>GR18D5212</b> Pedagogy Studies (Common to PE, DFM, VLSI, & CSE)
<b>DFM (52)</b>	<b>GR18D5131</b> Design of Hydraulics and Pneumatic Systems	<b>GR18D5129</b> Tool Design	<b>GR18D5128</b> Computer Aided Manufacturing	<b>GR18D5133</b> Automation in Manufacturing	<b>GR18D5212</b> Pedagogy Studies (Common to PE, DFM, VLSI, & CSE)
<b>VLSI (57)</b>	<b>GR18D5086</b> Analog CMOS IC Design	<b>GR18D5087</b> ASIC Design	<b>GR18D5092</b> Low Power VLSI Design	<b>GR18D5089</b> System on Chip Architecture	<b>GR18D5212</b> Pedagogy Studies (Common to PE, DFM, VLSI, & CSE)
			<b>GR18D5091</b> Digital Signal Processors and Architecture	<b>GR18D5090</b> Design for Testability	
<b>CSE (58)</b>	<b>GR18D5015</b> Advanced Algorithms	<b>GR18D5014</b> Machine Learning and Applications	<b>GR18D5019</b> Cloud Computing and Applications	<b>GR18D5018</b> Data Analytics	<b>GR18D5212</b> Pedagogy Studies (Common to PE, DFM, VLSI, & CSE)
					<b>GR18D5210</b> Value Education

**Note:** Any discrepancy may please be informed to COE immediately.

**Controller of Examinations**

Date: 06-08-2025