



**Gokaraju Rangaraju Institute of Engineering and Technology**  
(Autonomous)

GRIET/DOE/3H/G/16-17

11 Jan 2017

**M.Tech. I Year I Semester Supplementary Examinations (GR11) Time Table Feb 2017**

*TIME: 10.00 A.M TO 1.00 P.M*

Date Branch	07/02/2017 (Tuesday)	09/02/2017 (Thursday)	11/02/2017 (Saturday)	14/02/2017 (Tuesday)	16/02/2017 (Thursday)	18/02/2017 (Saturday)
<b>TE (21)</b>	<b>GR11D5131-</b> Advanced Thermodynamics	<b>GR11D5124-</b> Pressure Vessels and Piping ( <b>Elective-I</b> )	<b>GR11D5114-</b> Advanced Fluid Mechanics	<b>GR11D5132-</b> Advanced Heat and Mass Transfer	<b>GR11D5145-</b> Material Science and Technology ( <b>Elective-II</b> )	<b>GR11D5147-</b> Theory of Heat Pipes
<b>SE (25)</b>	<b>GR11D5036-</b> Distributed Databases ( <b>Elective-II</b> )	<b>GR11D5031-</b> Java and Web Technologies ( <b>Common to SE &amp; CSE</b> )	<b>GR11D5028-</b> Software Requirements and Estimation	<b>GR11D5030-</b> Software Process and Project Management	<b>GR11D5032-</b> Data warehousing and Data Mining ( <b>Common to SE &amp; CSE</b> ) ( <b>Elective-I</b> )	<b>GR11D5029-</b> Object Oriented Modeling ( <b>Common to SE &amp; CSE</b> )
<b>PE (43)</b>	<b>GR11D5051-</b> Analysis of Power Electronic Converters	<b>GR11D5053-</b> Power electronic Control of DC Drives	<b>GR11D5050-</b> Machine Modeling and Analysis	<b>GR11D5056-</b> Modern Power electronics ( <b>Elective-I</b> )	<b>GR11D5057-</b> PLCs and their applications ( <b>Elective-II</b> )	<b>GR11D5052-</b> Modern Control Theory
<b>DFM (52)</b>	<b>GR11D5071-</b> Advanced Mechanics of Solids	<b>GR11D5072-</b> Materials Technology	<b>GR11D5073-</b> Precision Engineering	<b>GR11D5075-</b> Special Manufacturing Process ( <b>Elective-I</b> )	<b>GR11D5078-</b> Advanced CAD ( <b>Elective-II</b> )	<b>GR11D5074-</b> Design for Manufacturing and Assembly
<b>ES (55)</b>	<b>GR11D5004-</b> Advanced Computer Communication Networks	<b>GR11D5001-</b> Microcontrollers for Embedded System Design ( <b>Common to ES &amp; VLSI</b> )	<b>GR11D5002-</b> Embedded Real Time Operating Systems	<b>GR11D5003-</b> VLSI Technology and Design ( <b>Common to ES &amp; VLSI</b> )	<b>GR11D5008-</b> Digital System Design ( <b>Common to ES &amp; VLSI</b> ) ( <b>Elective-II</b> )	<b>GR11D5007-</b> Advanced Digital Signal processing ( <b>Common to ES &amp; VLSI</b> ) ( <b>Elective-I</b> )

<b>Date</b> <b>Branch</b>	<b>07/02/2017</b> <b>(Tuesday)</b>	<b>09/02/2017</b> <b>(Thursday)</b>	<b>11/02/2017</b> <b>(Saturday)</b>	<b>14/02/2017</b> <b>(Tuesday)</b>	<b>16/02/2017</b> <b>(Thursday)</b>	<b>18/02/2017</b> <b>(Saturday)</b>
<b>VLSI</b> <b>(57)</b>	<b>GR11D5093-</b> Algorithms for VLSI Design Automation	<b>GR11D5001-</b> Microcontrollers for Embedded System Design <b>(Common to VLSI&amp; ES)</b>	<b>GR11D5016-</b> CPLD and FPGA Architectures and Applications	<b>GR11D5003-</b> VLSI Technology and Design <b>(Common to VLSI&amp; ES)</b>	<b>GR11D5008-</b> Digital System Design <b>(Common to VLSI&amp; ES)</b> <b>(Elective-I)</b>	<b>GR11D5007-</b> Advanced Digital Signal Processing <b>(Common to VLSI&amp; ES)</b> <b>(Elective-II)</b>
<b>CSE</b> <b>(58)</b>	<b>GR11D5102-</b> Embedded Systems and Programming	<b>GR11D5031-</b> Java and Web Technologies <b>(Common to CSE &amp; SE)</b>	<b>GR11D5101-</b> Advanced Problem Solving	<b>GR11D5103-</b> Computer System Design <b>(Elective-II)</b>	<b>GR11D5032-</b> Data Warehousing and Data Mining <b>(Common to CSE &amp; SE)</b> <b>(Elective-I)</b>	<b>GR11D5029-</b> Object Oriented Modeling <b>(Common to CSE &amp; SE)</b>
<b>Course Coordinator</b>						

**Note:** All course coordinators are requested to authenticate timetable while confirming the nomenclature of the subject is exactly as per Subject master index and syllabus (GR11). Any discrepancy may please be informed to DOE immediately.

**DOE**



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11 Jan 2017

**M.Tech. I Year II Semester Supplementary Examinations (GR11) Time Table Feb 2017**

*TIME: 10.00 A.M TO 1.00 P.M*

Date / Branch	08/02/2017 (Wednesday)	10/02/2017 (Friday)	13/02/2017 (Monday)	15/02/2017 (Wednesday)	17/02/2017 (Friday)	20/02/2017 (Monday)
<b>TE (21)</b>	<b>GR11D5135-</b> Energy Management	<b>GR11D5136-</b> Computational Methods in Heat Transfer	<b>GR11D5137-</b> Optimization Techniques	<b>GR11D5138-</b> Equipment Design for Thermal Systems	<b>GR11D5139-</b> Thermal and Nuclear Power Plants (Elective-III)	<b>GR11D5142-</b> Alternate Energy Sources (Elective-IV)
<b>SE (25)</b>	<b>GR11D5113-</b> Image Processing and fundamentals of Pattern Recognition (Elective-IV)	<b>GR11D5039-</b> Distributed Computing (Common to SE & CSE)	<b>GR11D5042-</b> Service Oriented Architecture	<b>GR11D5040-</b> Software Quality Assurance and Testing	<b>GR11D5041-</b> Software Architecture and Design Patterns	<b>GR11D5044-</b> Multimedia and Rich Internet Development (Elective-III)
<b>PE (43)</b>	<b>GR11D5061-</b> Power Electronic control of AC Drives	<b>GR11D5062-</b> Microprocessor and Microcontroller	<b>GR11D5063-</b> Flexible AC Transmission Systems	<b>GR11D5064-</b> Neural And Fuzzy Systems	<b>GR11D5066-</b> Power Quality (Elective-III)	<b>GR11D5069-</b> Embedded System Design (Elective-IV)
<b>DFM (52)</b>	<b>GR11D5082-</b> Design of Hydraulics and Pneumatics Systems	<b>GR11D5083-</b> Total Quality Management	<b>GR11D5084-</b> Computer Aided Manufacturing	<b>GR11D5085-</b> Design and Manufacturing of MEMS and Micro Systems	<b>GR11D5087-</b> Tool Design (Elective-III)	<b>GR11D5090-</b> Computational Fluid Dynamics
						<b>GR11D5091-</b> Automation in Manufacturing
<b>ES (55)</b>	<b>GR11D5013-</b> Hardware Software Co-Design	<b>GR11D5014-</b> Digital Signal Processors and Architectures (Common to ES & VLSI)	<b>GR11D5015-</b> System Modeling and Simulation	<b>GR11D5016-</b> CPLD and FPGA Architectures and Applications	<b>GR11D5017-</b> Low Power VLSI Design (Common to ES & VLSI) (Elective-III)	<b>GR11D5021-</b> System On Chip Architecture (Common to ES & VLSI) (Elective-IV)

<b>Date</b> <b>Branch</b>	<b>08/02/2017</b> <b>(Wednesday)</b>	<b>10/02/2017</b> <b>(Friday)</b>	<b>13/02/2017</b> <b>(Monday)</b>	<b>15/02/2017</b> <b>(Wednesday)</b>	<b>17/02/2017</b> <b>(Friday)</b>	<b>20/02/2017</b> <b>(Monday)</b>
<b>VLSI</b> <b>(57)</b>	<b>GR11D5098-</b> Design Of Fault Tolerant Systems	<b>GR11D5014-</b> Digital Signal Processors and Architectures <b>(Common to VLSI &amp;</b> <b>ES)</b> <b>( Elective-III )</b>	<b>GR11D5002-</b> Embedded Real Time Operating Systems	<b>GR11D5097-</b> CMOS Analog and Mixed Signal Design	<b>GR11D5017-</b> Low Power VLSI Design <b>(Common to ES &amp;</b> <b>VLSI)</b> <b>( Elective-IV )</b>	<b>GR11D5021-</b> System On Chip Architecture <b>(Common to VLSI and</b> <b>ES)</b>
<b>CSE</b> <b>(58)</b>	<b>GR11D5035-</b> Information Retrieval Systems <b>( Elective-III )</b>	<b>GR11D5039-</b> Distributed Computing <b>(Common to CSE &amp;</b> <b>SE)</b>	<b>GR11D5110-</b> Information Security <b>( Elective-IV )</b>	<b>GR11D5105-</b> Advanced Computer Networks	<b>GR11D5036-</b> Distributed Databases	<b>GR11D5104-</b> Advanced Compiler Design
<b>Course</b> <b>Coordinator</b>						

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