



**Gokaraju Rangaraju Institute of Engineering and Technology**  
(Autonomous)

<b>GR17</b> <b>Regulations</b>
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GRIET/DOE/3H/G/18-19

**M.Tech. I Year II Semester Supplementary Examinations (GR17) Time Table Dec/Jan 2018**

**TIME: 10.00 A.M TO 1.00 P.M**

Date Branch	03/01/2019 (Thursday)	08/01/2019 (Tuesday)	10/01/2019 (Thursday)	18/01/2019 (Friday)	19/01/2019 (Saturday)	21/01/2019 (Monday)
<b>STRU (20)</b>	<b>GR17D5162</b> Finite Element Methods in Structural Engineering	<b>GR17D5163</b> Structural Dynamics	<b>GR17D5165</b> Advanced Steel Design	<b>GR17D5187</b> Project Management (Open Elective-II)	<b>GR17D5167</b> Advanced Design of Structures (Elective-III)	<b>GR17D5170</b> Earthquake Resistant Design of Buildings (Elective-IV)
<b>TE (21)</b>	<b>GR17D5142</b> Advanced IC Engines	<b>GR17D5140</b> Convective Heat Transfer	<b>GR17D5141</b> Computational Methods in Heat Transfer	<b>GR17D5189</b> Non-Conventional Energy Resources (Common to TE & DFM) (Open Elective-II)	<b>GR17D5144</b> Thermal Measurements and Process Controls (Elective-III)	<b>GR17D5143</b> Equipment Design for Thermal Systems (Elective-IV)
<b>SE (25)</b>	<b>GR17D5012</b> Distributed Computing	<b>GR17D5029</b> Software Quality Assurance and Testing	<b>GR17D5030</b> Server Side Scripting Languages	<b>GR17D5185</b> Big Data Analytics (Common to SE & CSE) (Open Elective-II)	<b>GR17 D5007</b> Software Architecture and Design Patterns (Elective-III)	<b>GR17D5031</b> Multimedia and Rich Internet Development (Elective-IV)
<b>PE (43)</b>	<b>GR17D5036</b> Modern Power Electronics	<b>GR17D5047</b> Power Electronic Control of AC Drives	<b>GR17D5049</b> Flexible AC Transmission Systems (Common to PE & PS)	<b>GR17D5186</b> Neural and Fuzzy Systems (Common to PE & PS) (Open Elective-II)	<b>GR17D5051</b> Power Quality (Common to PE & PS) (Elective-III)	<b>GR17D5055</b> Programmable Logic Controllers (Common to PE & PS) (Elective-IV)
<b>DFM (52)</b>	<b>GR17D5118</b> Design of Hydraulics and Pneumatics Systems	<b>GR17D5119</b> Total Quality Management	<b>GR17D5120</b> Computer Aided Manufacturing	<b>GR17D5189</b> Non-Conventional Energy Resources (Common to TE & DFM) (Open Elective -II)	<b>GR17D5122</b> Industrial Robotics (Elective-III)	<b>GR17D5127</b> Automation in Manufacturing (Elective-IV)

<b>Date</b> <b>Branch</b>	<b>03/01/2019</b> <b>(Thursday)</b>	<b>08/01/2019</b> <b>(Tuesday)</b>	<b>10/01/2019</b> <b>(Thursday)</b>	<b>18/01/2019</b> <b>(Friday)</b>	<b>19/01/2019</b> <b>(Saturday)</b>	<b>21/01/2019</b> <b>(Monday)</b>
<b>ES</b> <b>(55)</b>	<b>GR17D5084</b> Digital Signal Processors and Architectures <b>(Common to ES &amp; VLSI)</b>	<b>GR17D5085</b> Embedded Networking	<b>GR17D5086</b> CPLD & FPGA Architectures and Applications	<b>GR17D5188</b> Hardware Software Co-Design <b>(Common to ES &amp; VLSI)</b> <b>(Open Elective-II)</b>	<b>GR17D5088</b> Wireless Communications and Networks <b>(Elective-III)</b>	<b>GR17D5092</b> Wireless LANs and PANs <b>(Elective-IV)</b>
<b>VLSI</b> <b>(57)</b>	<b>GR17D5084</b> Digital Signal Processors and Architectures <b>(Common to ES &amp; VLSI)</b> <b>(Elective-III)</b>	<b>GR17D5099</b> CAD for VLSI Circuits	<b>GR17D5100</b> CMOS Mixed Signal Circuit Design	<b>GR17D5188</b> Hardware Software Co-Design <b>(Common to ES &amp; VLSI)</b> <b>(Open Elective-II)</b>	<b>GR17D5101</b> Design for Testability	<b>GR17D5091</b> System On Chip Architecture <b>(Elective-IV)</b>
<b>CSE</b> <b>(58)</b>	<b>GR17D5001</b> Advanced Problem Solving	<b>GR17D5013</b> Advanced Data Mining	<b>GR17D5014</b> Cryptography and Network Security	<b>GR17D5185</b> Big Data Analytics <b>(Common to SE &amp; CSE)</b> <b>(Open Elective-II)</b>	<b>GR17D5017</b> Information Storage and Retrieval <b>(Elective-III)</b>	<b>GR17D5015</b> Advanced Computer Networks <b>(Elective-IV)</b>
<b>PS</b> <b>(83)</b>	<b>GR17D5065</b> Voltage Stability	<b>GR17D5066</b> Power System Operation and Deregulation	<b>GR17D5049</b> Flexible AC Transmission Systems <b>(Common to PE &amp; PS)</b>	<b>GR17D5186</b> Neural and Fuzzy Systems <b>(Common to PE &amp; PS)</b> <b>(Open Elective-II)</b>	<b>GR17D5051</b> Power Quality <b>(Common to PE &amp; PS)</b> <b>(Elective-III)</b>	<b>GR17D5055</b> Programmable Logic Controllers <b>(Common to PE &amp; PS)</b> <b>(Elective-IV)</b>

**Note:** Any discrepancy may please be informed to DOE immediately.

Date: 22/12/2018

  
DOE