



Gokaraju Rangaraju Institute of Engineering and Technology
(Autonomous)

GR11 Regulations

GRIET/DOE/3H/G/18-19 M.Tech. I Year II Semester Supplementary Examinations (GR11) Time Table Dec/Jan 2018


TIME: 10.00 A.M TO 1.00 P.M

Date Branch	03/01/2019 (Thursday)	08/01/2019 (Tuesday)	10/01/2019 (Thursday)	12/01/2019 (Saturday)	18/01/2019 (Friday)	19/01/2019 (Saturday)	21/01/2019 (Monday)
TE (21)	GR11D5135 Energy Management	GR11D5137 Optimization Techniques	GR11D5136 Computational Methods in Heat Transfer	-	GR11D5142 Alternate Energy Sources (Elective-IV)	GR11D5139 Thermal and Nuclear Power Plants (Elective-III)	GR11D5138 Equipment Design for Thermal Systems
SE (25)	GR11D5039 Distributed Computing (Common to SE & CSE)	GR11D5040 Software Quality Assurance and Testing	-	GR11D5042 Service Oriented Architecture	GR11D5113 Image Processing and fundamentals of Pattern Recognition (Elective-IV)	GR11D5041 Software Architecture and Design Patterns	GR11D5044 Multimedia and Rich Internet Development (Elective-III)
PE (43)	-	GR11D5061 Power Electronic control of AC Drives	GR11D5063 Flexible AC Transmission Systems	GR11D5062 Microprocessor and Microcontroller	GR11D5064 Neural And Fuzzy Systems	GR11D5066 Power Quality (Elective-III)	GR11D5069 Embedded System Design (Elective-IV)
DFM (52)	GR11D5082 Design of Hydraulics and Pneumatics Systems	GR11D5083 Total Quality Management	GR11D5084 Computer Aided Manufacturing	GR11D5085 Design and Manufacturing of MEMS and Micro Systems	GR11D5090 Computational Fluid Dynamics (Elective-IV)	GR11D5087 Tool Design (Elective-III)	GR11D5091 Automation in Manufacturing (Elective-IV)
ES (55)	GR11D5014 Digital Signal Processors and Architectures (Common to ES & VLSI)	GR11D5015 System Modeling and Simulation	GR11D5016 CPLD and FPGA Architectures and Applications	GR11D5017 Low Power VLSI Design (Common to ES & VLSI) (Elective-III)	GR11D5013 Hardware Software Co-Design	-	GR11D5021 System On Chip Architecture (Common to ES & VLSI) (Elective-IV)

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VLSI (57)	GR11D5014- Digital Signal Processors and Architectures (Common to VLSI & ES) (Elective-III)	GR11D5002- Embedded Real Time Operating Systems	GR11D5097- CMOS Analog and Mixed Signal Design	GR11D5017- Low Power VLSI Design (Common to ES & VLSI) (Elective-IV)	-	GR11D5098- Design Of Fault Tolerant Systems	GR11D5021- System On Chip Architecture (Common to VLSI and ES)
CSE (58)	GR11D5039- Distributed Computing (Common to CSE & SE)	GR11D5036- Distributed Databases	GR11D5110- Information Security (Elective-IV)	-	GR11D5104- Advanced Compiler Design	GR11D5035- Information Retrieval Systems (Elective-III)	GR11D5105- Advanced Computer Networks

Note: Any discrepancy may please be informed to DOE immediately

Date: 22/12/2018


DOE