



**Gokaraju Rangaraju Institute of Engineering and Technology**  
(Autonomous)  
**EXAMINATION BRANCH**

**II B.TECH I SEMESTER– GR15 REGULATIONS REGULAR\* EXAMINATIONS, NOV/DEC 2017**

**TIME TABLE**

**Time FN: 10:00 AM TO 01:00 PM**

Date Branch	13/11/2017, FN (Monday)	15/11/2017, FN (Wednesday)	17/11/2017, FN (Friday)	21/11/2017, FN (Tuesday)	23/11/2017, FN (Thursday)	25/11/2017, FN (Saturday)
CE	GR15A2002 Value Education and Ethics (Common to CE,ME & ECE)	GR15A2003 Building Materials and Construction Planning	GR15A2004 Electrical Technology	GR15A2005 Strength of Materials – I	GR15A2006 Surveying	GR15A2007 Fluid Mechanics
EEE	GR15A2001 Environmental Science (Common to EEE,CSE & IT)	GR15A2034 Electromagnetic Fields	GR15A2035 Network Theory	GR15A2036 DC Machines and Transformers	GR15A2058 Special Functions and Complex Variables	GR15A2076 Computer Organization
ME	GR15A2002 Value Education and Ethics (Common to CE,ME & ECE)	GR15A2011 Probability and Statistics (Common to ME,CSE & IT)	GR15A2019 Kinematics of Machinery	GR15A2020 Mechanics of Solids	GR15A2021 Engineering Thermodynamics	GR15A2022 Material Science and Metallurgy
ECE	GR15A2002 Value Education and Ethics (Common to CE,ME & ECE)	GR15A2043 Digital Electronics	GR15A2049 Signals and Systems	GR15A2048 Electronic Circuit Analysis	GR15A2047 Electrical Circuits	GR15A2050 Probability Theory and Stochastic Processes
CSE	GR15A2001 Environmental Science (Common to EEE,CSE & IT)	GR15A2011 Probability and Statistics (Common to ME,CSE & IT)	GR15A2062 Mathematical Foundation of Computer Science (Common to CSE & IT)	GR15A2063 Database Management Systems (Common to CSE & IT)	GR15A2064 Advanced Datastructures through C++ (Common to CSE & IT)	GR15A2065 Digital Logic Design (Common to CSE & IT)
IT	GR15A2001 Environmental Science (Common to EEE,CSE & IT)	GR15A2011 Probability and Statistics (Common to ME,CSE & IT)	GR15A2062 Mathematical Foundation of Computer Science (Common to CSE & IT)	GR15A2063 Database Management Systems (Common to CSE & IT)	GR15A2064 Advanced Datastructures through C++ (Common to CSE & IT)	GR15A2065 Digital Logic Design (Common to CSE & IT)

\* Treated as Supplementary for 2015 Admitted batch

Note: Any discrepancy may please be informed to DOE immediately.

DATE: 28/10/2017

  
DOE