



**Gokaraju Rangaraju Institute of Engineering and Technology**  
(Autonomous)

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| <b>GR15<br/>Regulations</b> |
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GRIET/COE/3H/G/18-19

**M.Tech. I Year I Semester Supplementary Examinations (GR15) Time Table July 2019**


*TIME: 10.00 A.M TO 1.00 P.M*

| Date<br>Branch       | 09/07/2019<br>(Tuesday)   | 11/07/2019<br>(Thursday)  | 16/07/2019<br>(Tuesday)  | 18/07/2019<br>(Thursday)   | 20/07/2019<br>(Saturday)   | 23/07/2019<br>(Tuesday)  | 24/07/2019<br>(Wednesday)  |
|----------------------|---|---|--|--|--|--|--|
| <b>STRU<br/>(20)</b> | <b>GR15D5158</b><br>Matrix Methods<br>in Structural<br>Analysis<br>(Elective-II)            | <b>GR15D5153</b><br>Theory and<br>Analysis of Plates                      | <b>GR15D5155</b><br>Advanced<br>Concrete<br>Technology<br>(Elective-I) | <b>GR15D5154</b><br>Advanced<br>Reinforced<br>Concrete Design                      | <b>GR15D5152</b><br>Theory of<br>Elasticity and<br>Plasticity                            | <b>GR15D5181</b><br>Computer-Oriented<br>Numerical Methods<br>in Engineering<br>(Open Elective-I)  | -  |
| <b>TE<br/>(21)</b>   | <b>GR15D5129</b><br>Advanced<br>Thermodynamics  | <b>GR15D5131</b><br>Advanced Fluid<br>Mechanics                           | <b>GR15D5130</b><br>Conduction and<br>Radiation Heat<br>Transfer       | <b>GR15D5132</b><br>Advanced Finite<br>Element Analysis<br>(Elective-I)            | <b>GR15D5136</b><br>Refrigeration<br>and Air-<br>Conditioning<br>(Elective-II)           | <b>GR15D5183</b><br>Operations<br>Research<br>(Common to TE &<br>DFM)<br>(Open Elective-I)         | <b>GR15D5133</b><br>Turbo<br>Machines<br>(Elective-I)                |
| <b>SE<br/>(25)</b>   | <b>GR15D5023</b><br>Software<br>Requirements<br>and Estimation                              | <b>GR15D5004</b><br>Distributed<br>Databases<br>(Elective-II)             | <b>GR15D5024</b><br>Software<br>Process and<br>Project<br>Management   | <b>GR15D5025</b><br>Component<br>Oriented<br>Programming<br>Languages              | -  | <b>GR15D5179</b><br>Enterprise Resource<br>Planning<br>(Common to SE<br>&CSE)<br>(Open Elective-I) | <b>GR15D5015</b><br>Advanced<br>Computer<br>Networks<br>(Elective-I) |
| <b>PE<br/>(43)</b>   | <b>GR15D5037</b><br>Analysis of<br>Power Electronic<br>Converters<br>(Common to PE<br>& PS) | <b>GR15D5039</b><br>Power Electronic<br>Control of DC<br>Drives           | <b>GR15D5040</b><br>Machine<br>Modeling &<br>Analysis                  | <b>GR15D5041</b><br>HVDC<br>Transmission<br>(Common to PE &<br>PS)<br>(Elective-I) | <b>GR15D5044</b><br>Solar and Wind<br>Energy<br>(Common to<br>PE & PS)<br>(Elective- II) | <b>GR15D5180</b><br>Modern Control<br>Theory<br>(Common to PE &<br>PS)<br>(Open Elective -I)       | -  |
| <b>DFM<br/>(52)</b>  | <b>GR15D5114</b><br>Advanced CAD<br>(Elective-II)   | <b>GR15D5111</b><br>Special<br>Manufacturing<br>Processes<br>(Elective-I) | <b>GR15D5108</b><br>Materials<br>Technology                            | <b>GR15D5109</b><br>Precision<br>Engineering                                       | <b>GR15D5107</b><br>Advanced<br>Mechanics of<br>Solids                                   | <b>GR15D5183</b><br>Operations<br>Research<br>(Common to TE &<br>DFM)<br>(Open Elective-I)         | -  |

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|----------------|--|---|--|---|--|---|---|
| ES<br>(55)     | GR15D5072<br>Embedded<br>System Design   | GR15D5073<br>Microcontrollers<br>for Embedded<br>System Design<br>(Common to ES<br>& VLSI)                  | GR15D5079<br>Digital System<br>Design<br>(Common to<br>ES & VLSI)<br>(Elective-II) | GR15D5074<br>Embedded Real<br>Time Operating<br>Systems                     | GR15D5075<br>Software<br>Defined Radio<br>(Elective-I)                                   | GR15D5182<br>Advanced Computer<br>Architecture<br>(Common to ES &<br>VLSI)<br>(Open Elective-I)   | -   |
| VLSI<br>(57)   | -  | GR15D5073<br>Microcontrollers<br>for Embedded<br>System Design<br>(Common to ES<br>& VLSI)<br>(Elective-II) | GR15D5079<br>Digital System<br>Design<br>(Common to<br>ES & VLSI)<br>(Elective-I)  | GR15D5086<br>CPLD and FPGA<br>Architectures and<br>Applications             | GR15D5094<br>CMOS Analog<br>Integrated<br>Circuit Design                                 | GR15D5182<br>Advanced Computer<br>Architecture<br>(Common to ES &<br>VLSI)<br>(Open Elective-I)   | GR15D5077<br>VLSI<br>Technology and<br>Design |
| CSE<br>(58)    | GR15D5012<br>Distributed<br>Computing  | -   | GR15D5003<br>Advanced Unix<br>Programming  | GR15D5010<br>Computer System<br>Design<br>(Elective-II)                     | GR15D5005<br>Multi-core<br>Computers:<br>Architecture and<br>Programming<br>(Elective-I) | GR15D5178<br>E-Commerce and<br>Applications<br>(Open Elective-I)<br>GR15D5179<br>Enterprise Resource<br>Planning<br>(Common to SE<br>& CSE)<br>(Open Elective -I) | GR15D5002<br>Object Oriented<br>Modeling      |
| PS<br>(83)     | GR15D5037<br>Analysis of<br>Power Electronic<br>Converters<br>(Common to PE<br>& PS) | GR15D5058<br>Advanced Power<br>System Analysis  | GR15D5059<br>Advanced<br>Power System<br>Protection                                | GR15D5041<br>HVDC<br>Transmission<br>(Common to PE &<br>PS)<br>(Elective-I) | GR15D5044<br>Solar and Wind<br>Energy<br>(Common to PE<br>& PS)<br>(Elective-II)         | GR15D5180<br>Modern Control<br>Theory<br>(Common to PE &<br>PS)<br>(Open Elective-I)  | -   |

Note: Any discrepancy may please be informed to COE immediately.

Date: 18/06/2019

  
Controller of Examinations