



**Gokaraju Rangaraju Institute of Engineering and Technology**  
(Autonomous)

GRIET/DOE/3H/G/17-18

10 July 2018

**M.Tech. I Year II Semester Supplementary Examinations (GR11) Time Table July/Aug 2018**

*TIME: 10.00 A.M TO 1.00 P.M*

Date Branch	24/07/2018 (Tuesday)	26/07/2018 (Thursday)	28/07/2018 (Saturday)	30/07/2018 (Monday)	01/08/2018 (Wednesday)	03/08/2018 (Friday)	07/08/2018 (Tuesday)
<b>TE (21)</b>	<b>GR11D5135</b> Energy Management	<b>GR11D5137</b> Optimization Techniques	<b>GR11D5136</b> Computational Methods in Heat Transfer	<b>GR11D5139</b> Thermal and Nuclear Power Plants (Elective-III)	<b>GR11D5138</b> Equipment Design for Thermal Systems	<b>GR11D5142</b> Alternate Energy Sources (Elective-IV)	-
<b>SE (25)</b>	<b>GR11D5039</b> Distributed Computing (Common to SE & CSE)	<b>GR11D5040</b> Software Quality Assurance and Testing	-	<b>GR11D5041</b> Software Architecture and Design Patterns	<b>GR11D5044</b> Multimedia and Rich Internet Development ( Elective-III )	<b>GR11D5113</b> Image Processing and fundamentals of Pattern Recognition ( Elective-IV )	<b>GR11D5042</b> Service Oriented Architecture
<b>PE (43)</b>	-	<b>GR11D5061</b> Power Electronic control of AC Drives	<b>GR11D5063</b> Flexible AC Transmission Systems	<b>GR11D5066</b> Power Quality ( Elective-III )	<b>GR11D5069</b> Embedded System Design ( Elective-IV )	<b>GR11D5064</b> Neural And Fuzzy Systems	<b>GR11D5062</b> Microprocessor and Microcontroller
<b>DFM (52)</b>	<b>GR11D5082</b> Design of Hydraulics and Pneumatics Systems	<b>GR11D5083</b> Total Quality Management	<b>GR11D5084</b> Computer Aided Manufacturing	<b>GR11D5087</b> Tool Design ( Elective-III )	<b>GR11D5091</b> Automation in Manufacturing (Elective-IV)	<b>GR11D5090</b> Computational Fluid Dynamics (Elective-IV)	<b>GR11D5085</b> Design and Manufacturing of MEMS and Micro Systems
<b>ES (55)</b>	<b>GR11D5014</b> Digital Signal Processors and Architectures (Common to ES & VLSI)	<b>GR11D5015</b> System Modeling and Simulation	<b>GR11D5016</b> CPLD and FPGA Architectures and Applications	-	<b>GR11D5021</b> System On Chip Architecture (Common to ES & VLSI) ( Elective-IV )	<b>GR11D5013</b> Hardware Software Co-Design	<b>GR11D5017</b> Low Power VLSI Design (Common to ES & VLSI) ( Elective-III )

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<b>VLSI</b> <b>(57)</b>	<b>GR11D5014-</b> Digital Signal Processors and Architectures <b>(Common to</b> <b>VLSI &amp; ES)</b> <b>( Elective-III )</b>	<b>GR11D5002-</b> Embedded Real Time Operating Systems	<b>GR11D5097-</b> CMOS Analog and Mixed Signal Design	<b>GR11D5098-</b> Design Of Fault Tolerant Systems	<b>GR11D5021-</b> System On Chip Architecture <b>(Common to</b> <b>VLSI and ES)</b>	-	<b>GR11D5017-</b> Low Power VLSI Design <b>(Common to ES</b> <b>&amp; VLSI)</b> <b>( Elective-IV )</b>
<b>CSE</b> <b>(58)</b>	<b>GR11D5039-</b> Distributed Computing <b>(Common to CSE</b> <b>&amp; SE)</b>	<b>GR11D5036-</b> Distributed Databases	<b>GR11D5110-</b> Information Security <b>( Elective-IV )</b>	<b>GR11D5035-</b> Information Retrieval Systems <b>( Elective-III )</b>	<b>GR11D5105-</b> Advanced Computer Networks	<b>GR11D5104-</b> Advanced Compiler Design	-

**Note:** Any discrepancy may please be informed to DOE immediately

  
DOE